

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:	Jyunichi NAKAMURA et al.	Confirmation No.:	
Serial No.:	To be assigned (Div. of SN 10/281,163)	Art Unit:	2826 (Assumed)
Filed:	August 28, 2003	Examiner:	A. WILLIAMS (Assumed)
For:	MULTILAYERED SUBSTRATE FOR SEMICONDUCTOR DEVICE	Attorney	7565-117-999
		Docket No:	

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination on the merits, please enter the following amendments and remarks into the file of the above-captioned application.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of the claims that begins on page 3 of this paper.

Remarks begin on page 6 of this paper.